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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/774,552	01/31/2001	Richard Dudley Baertsch	RD-27,948	3209

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GENERAL ELECTRIC COMPANY
GLOBAL RESEARCH
PATENT DOCKET RM. BLDG. K1-4A59
SCHENECTADY, NY 12301-0008

EXAMINER

AZARIAN, SEYED H

ART UNIT PAPER NUMBER

2625

DATE MAILED: 05/03/2004

8

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/774,552

Applicant(s)

BAERTSCH ET AL.

Examiner

Seyed Azarian

Art Unit

2625

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 05 February 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-47 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-7,9-23,25-34,36-42 and 44-47 is/are rejected.
- 7) ☒ Claim(s) 8,24,35 and 43 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 09 April 2001 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

RESPONSE TO AMENDMENT

1. Applicant's arguments, filed 2/5/2004, see page 8 through 12, with respect to the rejection of claims 1-47 under 103(a) have been fully considered and are persuasive. Therefore, the rejection has been withdrawn. However, upon further consideration, a new ground(s) of rejection is made in view of Polichar et al (U.S. patent 6,205,199).
2. Based on applicant's amendment persuasive arguments and upon further consideration the art rejection and double patenting rejection for claims 1-47, are hereby withdrawn.

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

4. Claims 1-7, 9-23, 25-34, 36-42 and 44-47, are rejected under 35 U.S.C. 102(e) as being anticipated by Polichar et al (U.S. patent 6,205,199).

Regarding claim 1, Polichar discloses a detector framing node receiving image data and communicating a portion of the image data to a host computer, comprising (column 12, lines 32-49, read out signal from the imager is synchronized with the digitizer of the image processor via serial communications port and host memory);

an image detection interface to receive image data in the form of at least one image frame having a predetermined size (column 4, lines 41-59, image processor received the digitized pixels transmitted by the serial interface drive of the controller, also column 8, lines 50-63);

a control unit to select a predetermined portion of the image data for storage and a memory unit to store the predetermined portion in response to tile selection by said control unit (column 12, line 56 through column 13, line 10, image display within one frame time and available storage media used by the control unit, also Fig. 4, column 11, lines 3-18, communication port and host memory).

Regarding claim 2, Polichar discloses the detector framing node according to claim 1, wherein the rate of communication between said image detection interface and said memory unit is greater than or equal to the rate of reception of the image data by said image detection interface (see claim 1, and column 3, lines 33-51, displaying the full dynamic range of an image-capturing).

Regarding claim 3, Polichar discloses the detector framing node according to claim 1, wherein said control unit is programmable to receive the image data from a selected flat panel detector of a plurality of different flat panel detectors (Fig. 6, column 10, lines 62-67, using a flat panel and column 11, lines 33-47, matrix flat panel display for use with the control unit).

Regarding claim 4, Polichar discloses the detector-framing node according to claim 3, wherein said control unit is communicates the received image data (column 12, lines 32-49, processor via serial communications port and host memory).

Regarding claim 6, Polichar discloses the detector-framing node according to claim 3, wherein the received image data is radioscopic image data and the select flat panel (Fig. 6,

column 10, lines 37-59, flat panel includes amorphous silicon and column 15, lines 53-67, image is the radiosopic image).

Regarding claim 7, Polichar discloses the detector-framing node according to claim 3, wherein a plurality of image frames is received continuously (column 13, lines 27-43, displaying images).

Regarding claim 10, Polichar discloses the detector-framing node according to claim 9, wherein said detector framing node transfers the received data to the host memory (column 13, lines 44-53, stores the digital image in a host buffer memory).

Regarding claim 11, Polichar discloses the detector interface receives the real time data (column 2, lines 54-67 real-time analog CRT image also column 9, lines 14-26).

Regarding claim 12, Polichar discloses the detector framing node according to claim 1, wherein the detector framing node is a PC I card and host Computer runs a non-real time operating system (column 11, lines 3-18, communication port and host memory).

Regarding claim 15, Polichar discloses an image system at least one host processor to execute operation with a host operation system and a computer communication interface to communicate (see claim 1, and column 12, line 56 through column 13, line 10, image display within one frame time and available storage media used by the control unit, processor via serial communications port and host memory).

Regarding claim 16, Polichar discloses the system according to claim 15, further comprising, a detector control unit to control communication of the image data between said image detection interface and said detector memory unit independently from the host operating system (column 13, lines 5-24, communication port, host memory and control unit).

Regarding claim 37, Polichar discloses the system according to claim 34, wherein said card communication the image data to the host memory along the computer communication bus at a data rate of at least 33 MHz (column 11, lines 20-32, rate of 33 MHz).

Regarding claims 5, 9, 13-14, 17-23 and 25, the arguments analogous to those presented for claims 1, 2 and 3 are applicable.

Regarding claims 26-30, the arguments analogous to those presented for claims 10,12 and 13 are applicable.

Regarding claims 31-34, 36, 38-42 and 44-47, the arguments analogous to those presented for claims 1, 6, 7 and 37, are applicable.

Allowable Subject Matter

5. Claims 8, 24, 35 and 43, are objected as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitation of the base claim and any intervening claims.

Contact Information

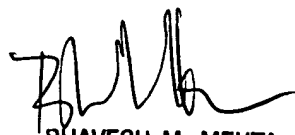
6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Seyed Azarian whose telephone number is (703) 306-5907. The examiner can normally be reached on Monday through Thursday from 6:00 a.m. to 7:30 p.m.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bhavesh Mehta, can be reached at (703) 308-5246. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application information Retrieval (PAIR) system. Status information for published application may be obtained from either Private PAIR or Public PAIR.

Status information about the PAIR system, see [http:// pair-direct.uspto.gov](http://pair-direct.uspto.gov). Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Seyed Azarian
Patent Examiner
Group Art Unit 2625
April 24, 2004



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